

22415 Microprocessor MCQs

Id	1
Question	In 8086 when Interrupt flag is set _____
A	The maskable interrupts are recognised by the CPU
B	The maskable interrupts are not recognised by the CPU
C	The non-maskable interrupts are recognised by the CPU
D	None of thses
Answer	A
Marks	1
Unit	1
Id	2
Question	In 8086 Parity flag is set when _____
A	The result contain even number of 1's
B	The result contain odd number of 1's
C	The result contain all zeros
D	both a and b
Answer	A
Marks	1
Unit	1
Id	3
Question	Which of the following is the 8 bit microprocessor?
A	8008
B	8080
C	8085
D	All of these
Answer	D
Marks	1
Unit	1

Id	4
Question	The I/O space of 8086 is _____ .
A	64 K Word ports
B	64 K byte ports
C	32 K byte ports
D	16 K Word ports
Answer	B
Marks	1
Unit	1
Id	5
Question	The 8086 is a _____ bit Microprocessor.
A	8 bit
B	32 bit
C	16 bit
D	24 bit
Answer	C
Marks	1
Unit	1
Id	6
Question	The operating modes of 8086 is _____
A	Real mode
B	Maximum mode
C	Minimum Mode
D	Both b and c
Answer	D
Marks	1
Unit	1

Id	7
Question	The 8086 has _____ data bus
A	32 bit
B	16 bit
C	64 bit
D	8 bit
Answer	B
Marks	1
Unit	1
Id	8
Question	The 8086 has _____ Address bus
A	20 bit
B	16 bit
C	32 bit
D	24 bit
Answer	A
Marks	1
Unit	1
Id	9
Question	The Instruction queue of 8086 is _____ Bytes
A	8 bytes
B	6 bytes
C	8 bytes
D	12 bytes
Answer	B
Marks	1

Unit	1
Id	10
Question	What is the maximum size of each memory segment in 8086?
A	64 kbyte
B	32 kbyte
C	60 kbyte
D	8 kbyte
Answer	A
Marks	1
Unit	1
Id	11
Question	What is the size of physical memory in 8086?
A	64 Kbyte
B	1 Mbyte
C	32 Kbyte
D	24 Kbyte
Answer	B
Marks	1
Unit	1
Id	12
Question	In 8086, _____ is the example for Non-maskable interrupt.
A	Trap
B	RST6.5
C	INTR
D	None of these
Answer	D
Marks	1

Unit	1
Id	13
Question	Which processor structure is pipelined?
A	all x80 processors
B	all x85 processors
C	all x86 processors
D	None of these
Answer	C
Marks	1
Unit	1
Id	14
Question	In 8086 the overflow flag is set when_____
A	The sum is more than 16 bits
B	Signed numbers go out of their range after an arithmetic operation
C	Carry and sign flags are set
D	During subtraction
Answer	B
Marks	1
Unit	1
Id	15
Question	What is meant by Maskable interrupts?
A	An interrupt which can never be turned off.
B	An interrupt that can be turned off by the programmer.
C	An interupt is always on
D	None of these
Answer	B
Marks	1

Unit	1
Id	16
Question	Which of the following is /are the functions of BIU?
A	To send the address of memory or I/O
B	To fetch instruction from memory
C	To support instruction queuing
D	All of these
Answer	D
Marks	1
Unit	1
Id	17
Question	The BIU of 8086 consist of _____
A	segment registers
B	instruction queue
C	Instruction Pointer
D	All of these
Answer	D
Marks	1
Unit	1
Id	18
Question	The Segment register of 8086 is _____ bit.
A	8 bit
B	16 bit
C	32 bit
D	20 bit
Answer	B
Marks	1

Unit	1
Id	19
Question	The queue of 8086 operates on the principle of _____
A	LIFO
B	FIFO
C	LILO
D	FILO
Answer	B
Marks	1
Unit	1
Id	20
Question	Feature of fetching the next instruction while the current instruction is executing is called _____
A	Fetching
B	Pipelining
C	Execution
D	Decoding
Answer	B
Marks	1
Unit	1
Id	21
Question	The EU unit of 8086 consist of _____
A	ALU
B	Flag register
C	instruction decoder
D	All of these
Answer	D
Marks	1

Unit	1
Id	22
Question	A register set of 8086 consists of _____
A	General purpose register
B	Pointer register
C	Index register
D	All of these
Answer	D
Marks	1
Unit	1
Id	23
Question	The flag register of 8086 is _____ bits
A	8 bits
B	32 bits
C	16 bits
D	24 bits
Answer	C
Marks	1
Unit	1
Id	24
Question	During physical address calculation the segment register content are shifted by _____
A	2-bits left
B	4-bits left
C	2-bits right
D	4-bits right
Answer	B
Marks	1

Unit	1
Id	25
Question	During instruction fetch _____ and _____ registers are used.
A	IP , DS
B	CS , IP
C	SS , BP
D	SS , IP
Answer	B
Marks	1
Unit	1
Id	26
Question	What is the size of TR in 80386?
A	16 bits
B	32 bits
C	48 bits
D	64 bits
Answer	A
Marks	1
Unit	1
Id	27
Question	_____ flag of 8086 is used for single stepping mode through a program
A	Zero flag
B	Trap Flag
C	Sign Flag
D	Interrupt Flag
Answer	B
Marks	1

Unit	1
Id	28
Question	Which of the following 8086 related statement is/are true?
A	segment can overlap
B	segment maximum size is 64 kbytes
C	segments minimum size is 16 bytes
D	All of these
Answer	D
Marks	2
Unit	1
Id	29
Question	_____ allow to use separate memory area for program, data, code and stack
A	Segmentation
B	Pipelining
C	Both a and b
D	None of these
Answer	A
Marks	1
Unit	1
Id	30
Question	For the stack operation in 8086 _____ register are used?
A	SS
B	SP
C	BP
D	All of these
Answer	D
Marks	1

Unit	1
Id	31
Question	What is the size of physical address in 8086?
A	16 bit
B	20 bit
C	24 bit
D	32 bit
Answer	B
Marks	1
Unit	1
Id	32
Question	The contents of different registers are given below. AX = 1000H, BX = 2000H, SI = 3000H, DI = 4000H, BP = 5000H, SP = 6000H, CS = 0000H, DS = 1000H, SS = 2000H, IP = 7000H. Calculate physical address for MOV AX, [5000H].
A	5000H
B	15000H
C	10500H
D	1000500H
Answer	B
Marks	2
Unit	1
Id	33
Question	The contents of different registers are given below. AX = 1000H, BX = 2000H, SI = 3000H, DI = 4000H, BP = 5000H, SP = 6000H, CS = 0000H, DS = 1000H, SS = 2000H, IP = 7000H. Calculate physical address for MOV AX, [BP] [SI]
A	18000H
B	15000H
C	12000H

D	28000H
Answer	D
Marks	2
Unit	1
Id	34
Question	The contents of different registers are given below. AX = 1000H, BX = 2000H, SI = 3000H, DI = 4000H, BP = 5000H, SP = 6000H, CS = 0000H, DS = 1000H, SS = 2000H, IP = 7000H. Calculate physical address for MOV AX, 5000H [BX] [SI]
A	15000H
B	10000H
C	0A000H
D	1A000H
Answer	D
Marks	2
Unit	1
Id	35
Question	What is the output of the following code AL=88 BCD, CL=49 BCD ADD AL,CL DAA
A	D7, CF=1
B	37, CF=1
C	73, CF=1
D	7D, CF=1
Answer	B
Marks	2
Unit	1
Id	36

Question	What is the output of the following code AL= 49 BCD, BH= 72 BCD SUB AL,BH DAS
A	AL=D7, CF=1.
B	AL=7D, CF=1.
C	AL=77, CF=1
D	none of them
Answer	C
Marks	2
Unit	1
Id	37
Question	What is opcode?
A	The instruction that is to be executed
B	The value in which an operation acts upon
C	A mnemonic that defines a data size
D	The compiled assembly code
Answer	A
Marks	1
Unit	1
Id	38
Question	What are the names of the 4 segment registers?
A	Data, Index, Code, Stack
B	Stack, Index, Extra, Code
C	Stack, Data, Base, Counter
D	Stack, Extra, Code, Data
Answer	D
Marks	1

Unit	1
Id	39
Question	The 8086/8088 used two processing units which were known as:
A	Left and Right Units
B	Segment and Offset Units
C	Bus Unit and Execution Interface Unit
D	Bus Interface Unit and Execution Unit
Answer	D
Marks	1
Unit	1
Id	40
Question	The first processor to include Virtual memory in the Intel microprocessor family was:
A	4004
B	80286
C	80386
D	80486
Answer	B
Marks	1
Unit	1
Id	41
Question	The Effective address range in 8086 is _____.
A	0000H to FFFFH
B	00000H to FFFFFH
C	0000H to 0FFFFH
D	None of these
Answer	A
Marks	1

Unit	1
Id	42
Question	Which segment register is used along with BP register to calculate Physical address?
A	CS
B	SS
C	DS
D	ES
Answer	B
Marks	1
Unit	1
Id	43
Question	Can ROM be used as stack?
A	Yes
B	No
C	Sometimes yes
D	Sometimes no
Answer	B
Marks	1
Unit	1
Id	44
Question	The zero flag is reset
A	When the result of last arithmetic/logical instruction is zero
B	When the result of last arithmetic/logical instruction is not zero
C	When the result of last arithmetic/logical instruction produces carry out of MSB.
D	None of these
Answer	B
Marks	1

Unit	1
Id	45
Question	What is the size of IP in 8086 is _____
A	32
B	16
C	48
D	Both a and b
Answer	B
Marks	1
Unit	1
Id	46
Question	Microprocessor speed depends on _____
A	Clock
B	Data bus width
C	Address bus width
D	Size of register
Answer	A
Marks	1
Unit	1
Id	47
Question	What is the range of Memory locations in 8086?
A	00000H to FFFFFH
B	0000H to FFFFH
C	000000H to FFFFFFFH
D	00000H to FFFFH
Answer	A
Marks	1

Unit	1
Id	48
Question	In 8086 when Auxiliary Flag is set _____
A	If there is carry from lower nibble
B	If there is no carry from lower nibble
C	Both a and b
D	None of these
Answer	A
Marks	1
Unit	1
Id	49
Question	In 8086 when Trap flag is set _____
A	The processor enter the free run mode
B	The processor enter the single step execution mode
C	Both a and b
D	none of these
Answer	B
Marks	1
Unit	1
Id	50
Question	8088 differs 8086 in _____
A	Data width on the output
b	Supports of Co processor
C	Address capability
D	Supports of max/min mode.
Answer	A
Marks	1

Unit	1
Id	51
Question	For 8086 _____ bus is bidirectional and _____ bus is unidirectional
A	Address, data
B	Data , address
C	Control, data
D	Address, control
Answer	B
Marks	1
Unit	1
Id	52
Question	What is the function of segment registers?
A	To store the starting address of corresponding segment
B	To store data required for arithmetic or logical operations
C	To store address within the segment
D	All of above
Answer	A
Marks	1
Unit	1
Id	53
Question	What is the address range of I/O space in 8086?
A	0000h – 0FFFh
B	00000h – FFFFFh
C	0000h – FFFFh
D	None of these
Answer	C
Marks	1

Unit	1
Id	54
Question	Which registers are used to generate physical address from logical address?
A	Segment registers
B	Offset register
C	Both (a) and (b)
D	General purpose register
Answer	C
Marks	1
Unit	1
Id	55
Question	ALE stands for _____
A	Address latch enable
B	Address length enable
C	Address lower enable
D	Address last enable
Answer	A
Marks	1
Unit	1
Id	56
Question	Which flag is not present in 8086 microprocessor but present in 80386 microprocessor?
A	Zero Flag
B	Sign Flag
C	Trap Flag
D	Nested Task Flag
Answer	D
Marks	1

Unit	1
Id	57
Question	Which of the following is not possible by a microprocessor?
A	Reading from Memory
B	Writing into Memory
C	Reading from Input port
D	Writing into Input port
Answer	D
Marks	1
Unit	1
Id	60
Question	A 32-bit processor has
A	32 register
B	32 I/O devices
C	32 Mb of RAM
D	32bit ALU and 32-bit registers
Answer	D
Marks	1
Unit	1
Id	61
Question	A machine cycle refers to
A	fetching an instruction
B	clock speed
C	fetching, decoding and executing an instruction
D	executing an instruction
Answer	C
Marks	1

Unit	1
Id	62
Question	The system bus is made up of _____
A	Data bus
B	Data bus and address bus
C	Data bus and control bus
D	Data bus, control bus and address bus
Answer	D
Marks	1
Unit	1
Id	63
Question	The minimum number of bits required to store the hexadecimal number FFH is _____
A	2
B	4
C	8
D	16
Answer	C
Marks	1
Unit	1
Id	64
Question	A megabyte represents _____
A	1 million bytes
B	1024 kilobytes
C	2^{30} bytes
D	1024 bytes
Answer	B
Marks	1

Unit	1
Id	65
Question	The ASCII code of 'A' is _____
A	66D
B	41H
C	0100 0010
D	0110 0011
Answer	B
Marks	1
Unit	1
Id	66
Question	Why 80386 processor is called as 32 bit processor?
A	Because 80386 processor has 32 bit ALU
B	Because 80386 processor has 32 bit data bus
C	Both(a) and (b)
D	None of these
Answer	C
Marks	1
Unit	1
Id	67
Question	How many break point addresses we can load in debug registers of 80386 microprocessor?
A	4
B	5
C	7
D	8
Answer	A
Marks	1

Unit	1
Id	68
Question	What is the use of base registers and offset registers?
A	To hold 16-bit relative address present within the segment
B	To perform arithmetic and logical operations
C	To hold the data temporary
D	All of above
Answer	A
Marks	1
Unit	1
Id	69
Question	If direction flag is set then
A	SI is automatically decremented
B	SI is automatically incremented
C	DI is automatically decremented
D	Both (a) and (c)
Answer	D
Marks	1
Unit	1
Id	70
Question	If PE = 1, then 80386 DX microprocessor operates in _____
A	Real mode
B	Protected mode
C	Virtual 86 mode
D	Special mode
Answer	B
Marks	1

Unit	1
Id	71
Question	READY# is _____
A	Address Bus Ready signal
B	Data bus ready signal
C	Control Bus ready signal
D	All of the above
Answer	B
Marks	1
Unit	1
Id	72
Question	In which microprocessor does the concept of pipeline introduced?
A	80186
B	80286
C	80386
D	80486
Answer	A
Marks	1
Unit	1
Id	73
Question	The parity bits are used to check that a _____
A	Two bit error
B	Single bit error
C	Multi bit error
D	None of these
Answer	B
Marks	1

Unit	1
Id	74
Question	_____ is the most important segment and it contains the actual assembly language instruction to be executed by the microprocessor:
A	Data segment
B	Code segment
C	Stack segment
D	Extra segment
Answer	B
Marks	1
Unit	1
Id	75
Question	A microprocessor contains _____
A	most of RAM
B	most of ROM
C	peripheral drivers
D	most of the control and arithmetic logic functions of computer
Answer	D
Marks	1
Unit	1

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